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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Hieu Van Tran et al.

Serial No.: 10/044,821

Group Art Unit: 2818

Filed: January 10, 2002

Examiner: Not Yet Assigned

Title: **BIAS DISTRIBUTION NETWORK FOR DIGITAL MULTILEVEL  
NONVOLATILE FLASH MEMORY**

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*Rosa A. Caviedes*

Rosa A. Caviedes

\* \* \*

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Before examination of this application, please amend the drawings as follows:

IN THE DRAWINGS:

In Figure 2:

The Ispbias box has been labeled 202.

The IBNSP<0> connected to the box 208 on the right side of the page has been changed to IBNSP<4>.

In Figure 3:

The inverter 378 was corrected to be "inverter 375".

**REMARKS**

This amendment is submitted with a Response to Notice to File Corrected Application Papers, which submits therewith formal drawings. The proposed changes to the drawings are circled in red in the attached Figures 2 and 3. No new matter has been added.

Please address all future communications regarding this application to:

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Please charge any additional fees, including any fees necessary for extensions of time, or credit overpayment to Deposit Account No. 07-1896.

Dated: April 5, 2002

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FIG. 29

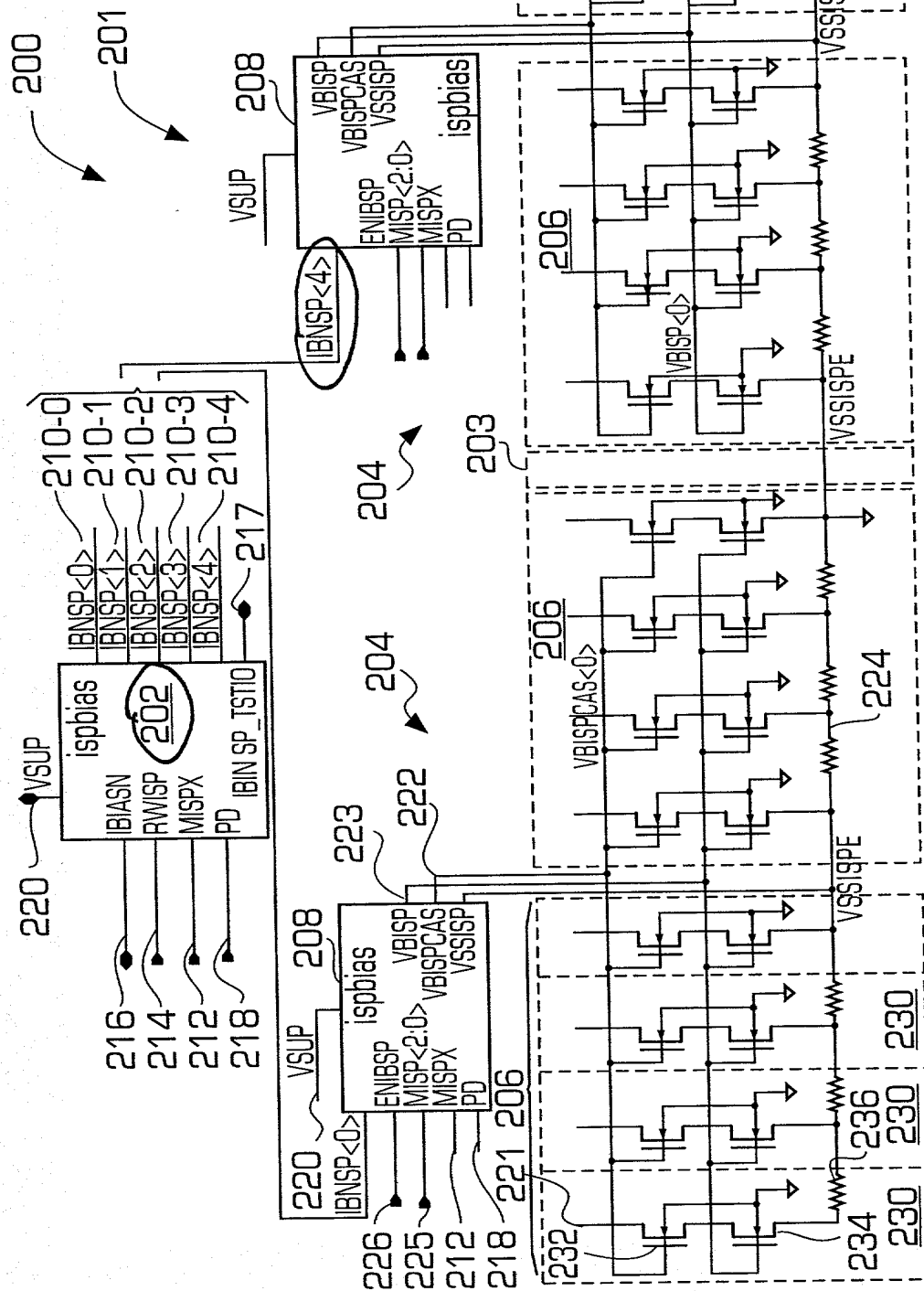


FIG. 2b

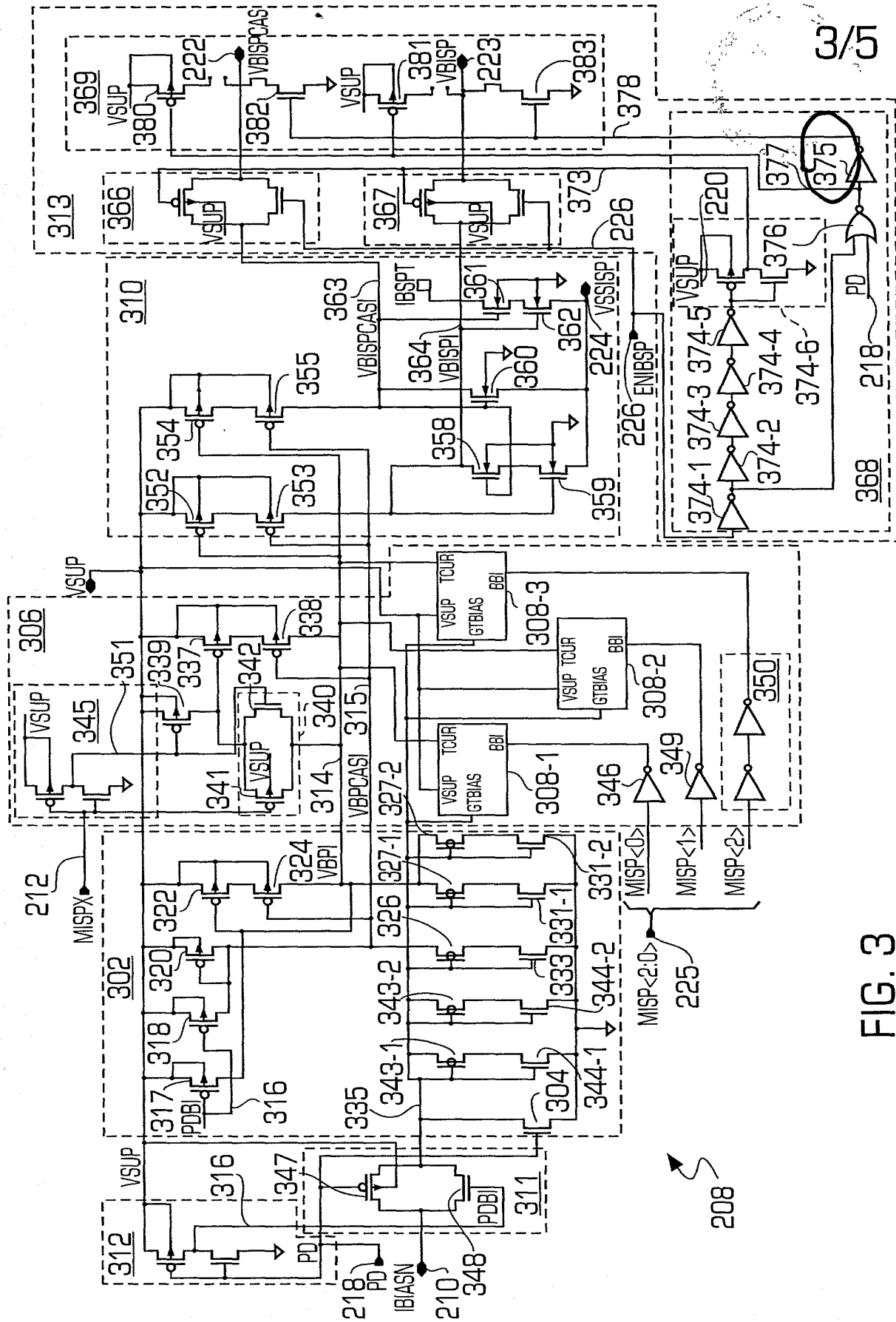


FIG. 3